USN

Third Semester B.E. Degree Examination, Dec.2013/Jan.2014 Analog Electronic Circuits

Time: 3 hrs.

c.

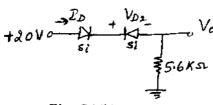
Max. Marks:100

Note: 1. Answer FIVE full questions, selecting at least TWO questions from each part.
2. Missing data, if any, may be assumed suitably.

PART - A

- a. Explain the working of full wave rectifier circuit with relevant waveforms. Assume the diodes of silicon. Also find the PIV rating of each diode. (08 Marks)
 - b. Determine I_D , V_{D2} and V_0 for the network shown in Fig. Q1(b).

(06 Marks)



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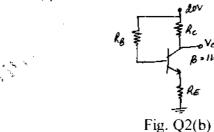
Fig. Q1(b)

Fig. Q1(c)

Obtain the output waveform for the circuit shown in Fig. Q1(c)

(06 Marks)

- 2 a. Derive expressions for I_B, I_C, I_E, V_B, V_C, V_E, V_{CE} and I_{csat} in a BJT voltage divider bias circuit. Use exact analysis. (08 Marks)
 - b. For the bias configuration as shown in Fig. Q2(b), determine R_C , R_E and R_B , if $I_{CQ} = \frac{1}{2} I_{csat}$, $I_{csat} = 8 \text{ mA}$ and $V_C = 18 \text{ V}$, $\beta = 110$. (06 Marks)



c. Calculate stability factor and change in collector current from 25°C to 100°C for the transistor having the following parameters with emitter bias:

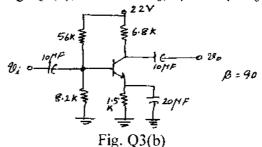
$$\frac{R_B}{R_E} = 10$$
, $\beta_{25^{\circ}C} = 50$, $\beta_{100^{\circ}C} = 80$, $I_{CO_{25^{\circ}C}} = 0.1 \text{ nA}$, $I_{CO_{100^{\circ}C}} = 20 \text{ nA}$ (06 Marks)

3 a Derive the Hybrid equivalent circuit of a BJT.

(06 Marks)

b. For the network shown in Fig. Q3(b), determine Z_0 , Z_i and A_v if $r_0 = 50 \text{ k}\Omega$.

(0**6 Mar**ks)



c. Derive an expression for the voltage gain of a CE BJT amplifier considering the effect of source and load resistances. (08 Marks)

4 a. Explain the general frequency response of an amplifier.

(06 Marks)

b. Determine the lower cut off frequency and midband gain for the RC coupled amplifier shown in Fig. Q4(b). (06 Marks)

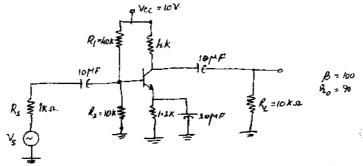


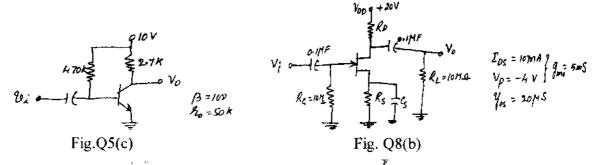
Fig. Q4(b)

c. Derive expressions for Miller effect input and output capacitances.

(08 Marks)

PART - B

- 5 a. Derive expressions for DC bias currents and voltages in a Darlington connection of BJTs.
 (06 Marks)
 - b. Explain the types of feedback amplifiers. Also derive expression for input and output impedances of any one type. (08 Marks)
 - c. For the network shown in Fig.Q5(c), determine Z_0 , Z_i and A_v . (06 Marks)



6 a. Explain the transformer coupled class-A amplifier.

(04 Marks)

b. Explain the role of heat sink in power transistor.

(04 Marks)

- c. For a class B amplifier proving 20 V peak signal to a 16 Ω speaker and a power supply of $V_{CC} = 30V$, determine the input power, output power and efficiency. Also find their maximum values. (12 Marks)
- 7 a. Explain the Barkhauson criterion for oscillations.

(04 Marks)

b. What are tuned oscillators? Explain any one type of tuned oscillator.

(06 Marks)

c. Explain the working of series resonant crystal oscillator.

(06 Marks)

- d. Determine the frequency of a BJT phase shift oscillator, if $R = 3 \text{ k}\Omega$, C = 1500 pF and $R_c = 12 \text{ k}\Omega$. (04 Marks)
- 8 a. Derive expressions for, Z_i, Z_o and A_v of a common drain configuration of JFET source follower. (08 Marks)
 - b. Determine R_D and R_S for the network shown in Fig. Q8(b) to establish a gain of 8 if the bypass capacitor C_s is removed. (06 Marks)
 - c. Explain the small signal models of D-MOSFET and E-MOSFET.

(06 Marks)